

Figure 1

## Main Reader Amplifier First Stage Simplified Schematic

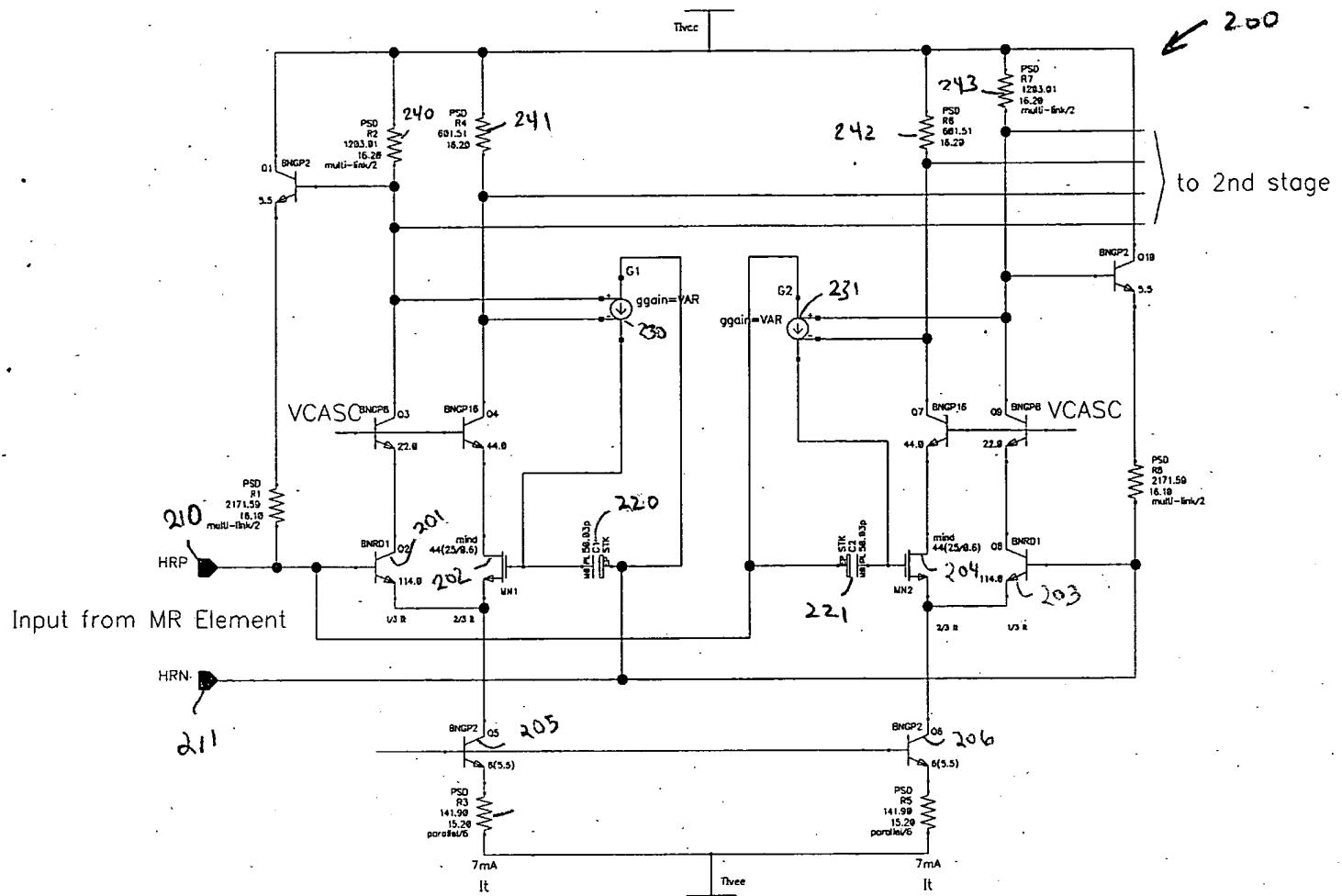


Figure 2